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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/024,006	12/21/2001	Morgan Gustavsson	010315-186	4452

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ERICSSON INC.
6300 LEGACY DRIVE
M/S EVR C11
PLANO, TX 75024

EXAMINER

DALEY, CHRISTOPHER ANTHONY

ART UNIT	PAPER NUMBER
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2111

DATE MAILED: 07/13/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/024,006

Applicant(s)

GUSTAVSSON, MORGAN

Examiner

Christopher A Daley

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 December 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1- 12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☒ Certified copies of the priority documents have been received in Application No. 10/024,006.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Drawings

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawing sheets are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

2. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Appropriate correction is required.

3. Claims 1 - 12 are examined.

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4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1 – 12 are rejected under 35 U.S.C. 102(b) as being anticipated by Ilkbahar (US6026456).

As to claims 1 and 12, Ilkbahar discloses:

- an arrangement and method for data transmission in an expandable modular system (see Figure 2, wherein modules 210, 220 represent expandable modules for system 200),
- preferably for point-to-multipoint transmission, formed of a plurality of structurally and electrically connectable modules (210, 210, 250 of Figure 2 where module 210 communicates with multiple modules such as 250, 260)
- between each module being located databus connectors(modules are connected to bus 205),
- which under operation allow connection and disconnection of the modules (Figure 5 and Col. 7 lines 52 – Col. 8, line 48 illustrate how the control circuit 520 enable/disable modules such as 210, 220, 250, 260, etc from being connected or disconnected).,
- each databus connector being connected to a driver(212) and a receiver(Col. 1, lines 56 –67, where each module connection to a bus comprises a receiver/transceiver in order to communicate) arrangement,
- each comprising output and input channels, the outputs (212) being connected to the inputs (input with coupled to termination device 230), where for each module the arrangement comprises a signal termination arrangement (216, and 232) arranged outside the driver(212) and receiver (on bus 205) between the output channels of the driver arrangement (210) and the input channels of the receiver arrangement (205).

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6. As to claims 2, and 9 Ilkbahar discloses an arrangement where the output channels of the driver arrangement are connectable to corresponding output channels of another driver arrangement of another module. (Output of devices 210 and 250 are drivers on a bus 205 that are part of other cluster logic, multiple drivers, Col. 4, lines 36 - 40).

7. As to claim 3, Ilkbahar discloses an arrangement where the signal termination arrangement comprises an impedance load. (The termination agents are transistors biased to be resistors (Figure 5). Different types of terminating agents are possible (Col. 4, lines 42 - 55).

8. As to claims 4,9 and 10, Ilkbahar discloses an arrangement and a backplane where the termination arrangement is arranged directly between the output channels of the driver arrangement and/or the input channels of the receiver arrangement. (Figure 2 shows the termination device (216) coupled to the output of the driver (212). The termination device (230 is coupled to input on bus 205. In the case of a backplane, Figure 3 shows I/Os of module 310 and 315 coupled to bus 305).

9. As to claim 5, Ilkbahar discloses an arrangement where the module is a carrier of electronic components. (Figure 3 shows module 322 being a carrier of an electronic component 324, Col. 4, lines 16 - 18).

10. As to claim 6, Ilkbahar discloses an arrangement where the module is a backplane (Figure 3 shows a backplane 300 with several circuit cards (324, 332,342, 352 inserted).

11. As to claim 7, and 11 Ilkbahar discloses of an arrangement and a backplane where the data transmission is high bit rate transmission (The distributed termination system allows for high speed performance as reflections and noise are eliminated, regardless if termination system is implemented in a module or on a backplane. Col. 3, lines 64 - 65).

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12. As to claim 8, Ilkbahar discloses a backplane unit (300, Figure 3), preferably for point-to-multipoint transmission, for structurally and electrically interconnection to a second backplane unit, between each backplane unit being located wiring and databus connectors, which allow plug-in and removal of a module, preferably under operation, each databus connector being connected to a driver and a receiver arrangement, each comprising output and input channels, the outputs being connected to the inputs, where between the output channels of the driver arrangement and the input channels of the receiver arrangement and outside the driver and receiver arrangements, a signal termination arrangement is arranged. (Figure 3 displays a system where multiple circuit boards (324, 332, 342, and 352) are coupled to a motherboard (300). These circuit cards have integrated circuit devices mounted in them that contain modules that have the distributed termination system embedded in them. All these boards are coupled to the bus 305. These cards are added to upgrade the motherboard performance. With the pull up and tri-state features in the termination agents, boards can be added and removed from the motherboard without affecting system operation, Col. 6, lines 21 - 42).

Conclusion

13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher A Daley whose telephone number is 703 605 4214. The examiner can normally be reached on 9 a.m. – 4 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 703 305 4815. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



CAD
July 1, 2004



TIMVO
PRIMARY EXAMINER